Design Guide:

Dort	Item	Explanation		Grade		
Part				Standard	Semi-Prem.	Premium
Conductor	Line width	¥		\geq 0.3mm	≧ 0.2mm	≧ 0.1mm
	Line space	Y		≧ 0.3mm	≧ 0.2mm	≧ 0.1mm
	Subs edge to pattern edge	¥ ====		≧ 0.5mm	≧ 0.4mm	≧ 0.3mm
	T/hole land	¥ © ¥ ©		≧ 0.35mm	≥ 0.3mm	≧ 0.25mm
Hole	T/hole dia.	*		≧ 0.5mm	≧ 0.4mm	≧ 0.3mm
	T/hole dia.	▼ B	Α	≧ 0.5mm	≧ 0.4mm	≧ 0.3mm
			В	≧ 0.6mm ~	≥ 0.5~0.6mm	≧ 0.4~ 0.5mm
	Hole to hole pitch	→ 0		≧ Substrate thickness		
	Subs edge to pattern edge	<u>V</u>		≧ Substrate thickness		
Multilayer Print	Print Tolerance	*		≧ 0.2mm	≧ 0.15mm	≧ 0.1mm
	Subs edge to pattern edge	<u> </u>		≧ 0.5mm	≧ 0.4mm	≧ 0.3mm

Part	Item	Explanation	Grade			
i ait	пеш		Standard	Semi-Prem.	Premium	
Special Shape	Via filling		Φ 0.4mm	Φ0.3mm	Φ0.2mm	
			For land dia., to be discussed			
	T/hole print		To be discussed			
	Subs. 凸凹 shape					
	Print on 凸凹 part					
	T/hole print on 凸凹 area					